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PATENT

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Inventor application of: Suaya & Gabillet

Art Unit: 2858

Application No. 09/385,666

Technology Center 2100

Filed: August 26, 1999


CERTIFICATE OF MAILING

For: CAPACITANCE MEASUREMENTS FOR
AN INTEGRATED CIRCUIT

Examiner: Not yet assigned

Date: August 17, 2001

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service on August 17, 2001 as First Class Mail in an envelope addressed to: COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231.


Attorney for Applicant

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INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)(3)

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Sir:

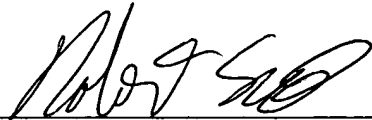
Listed on the accompanying form PTO-1449 and enclosed herewith are several English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent.

Applicants filed this Information Disclosure Statement ("IDS") before the mailing date of a first Office action on the merits. As a result, no fee should be required to file this IDS. However, if the Patent Office determines that a fee is required for Applicants to file this Information Disclosure Statement, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550. A **duplicate** copy of this Information Disclosure Statement is enclosed.

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Respectfully submitted,

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cc: Docketing

Client (106-007)